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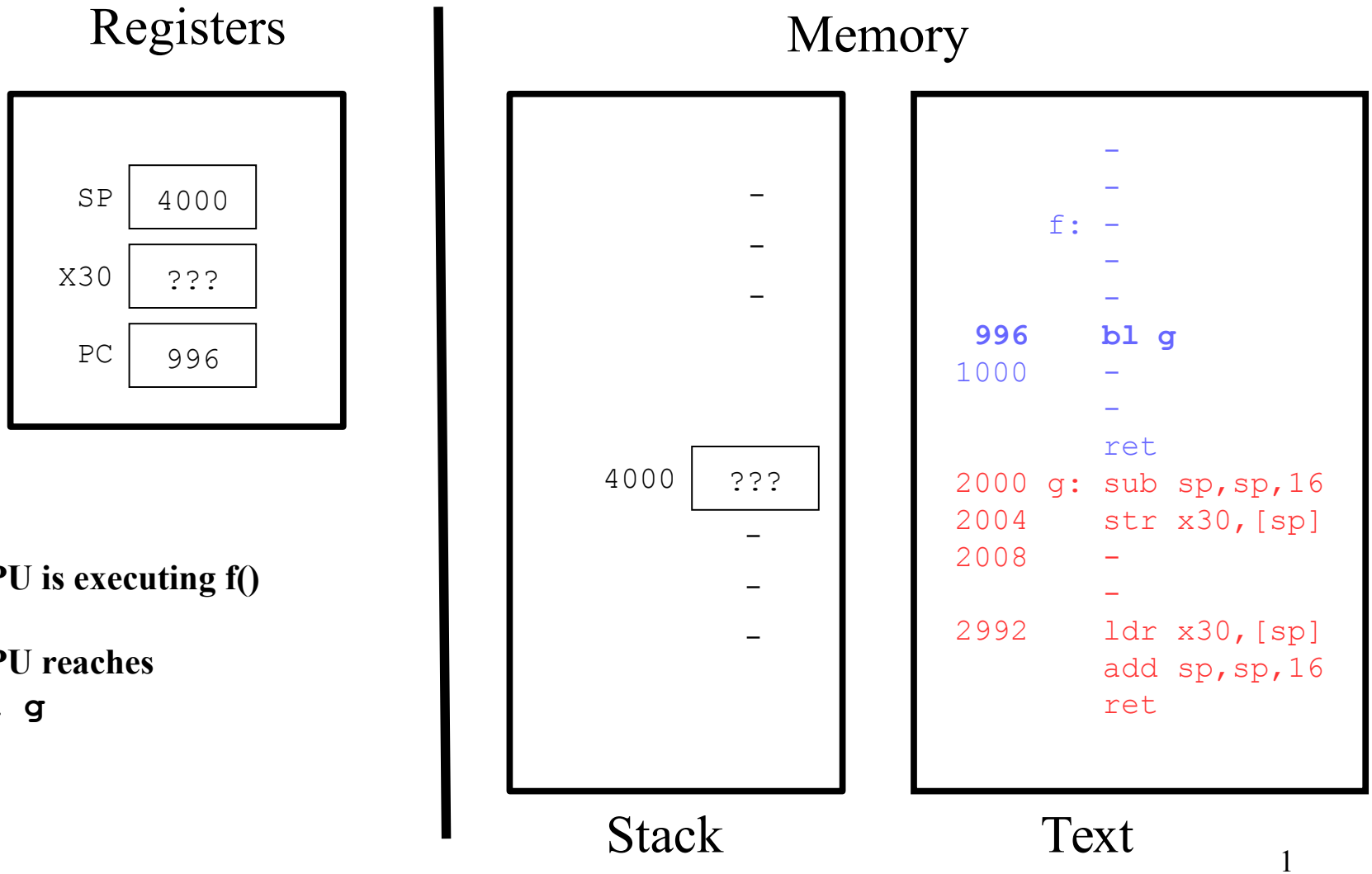
Trace of an ARMv8 Assembly Language Function Call



CPU is executing f()

CPU reaches
`bl g`

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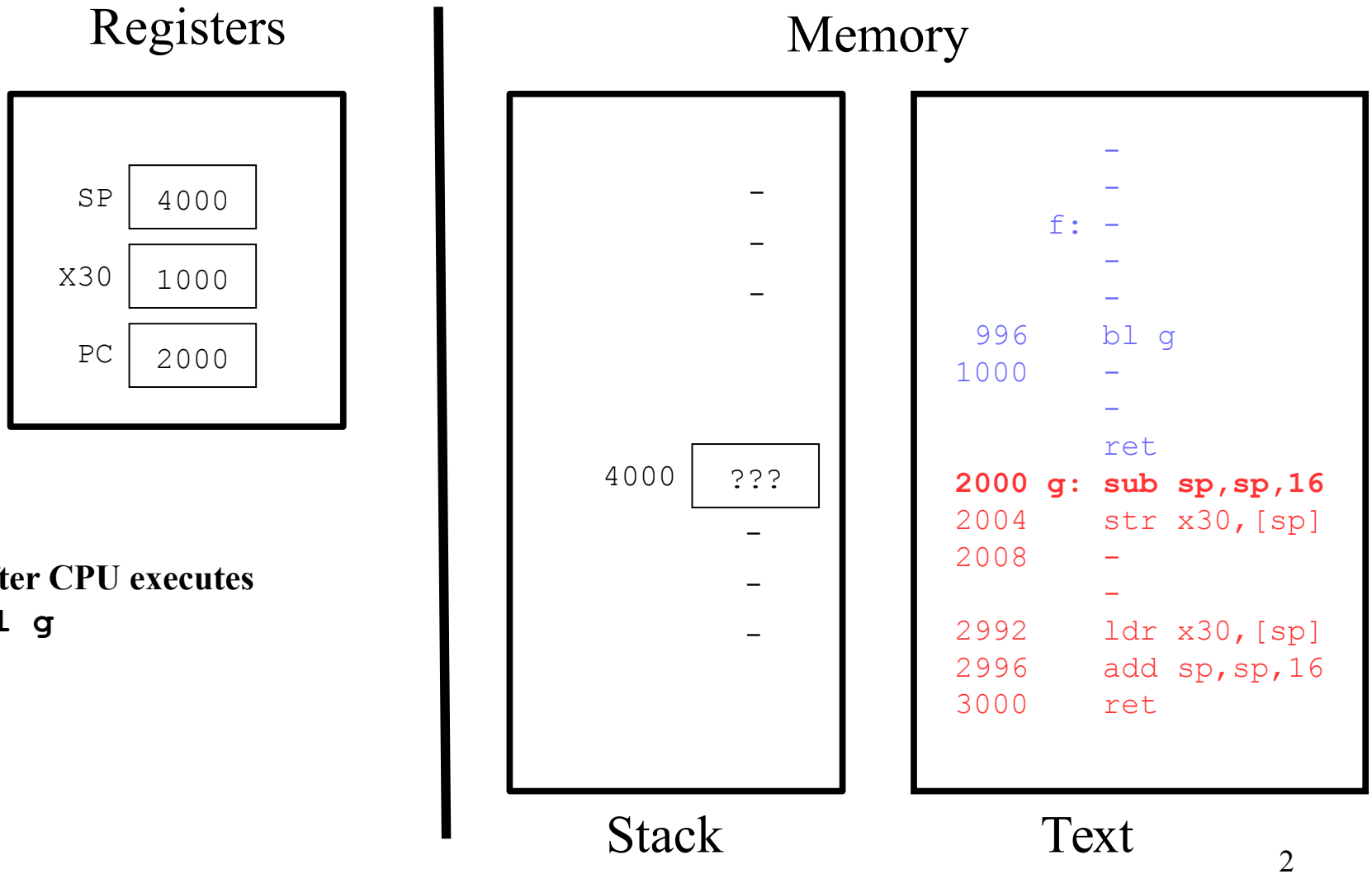

After CPU executes
`bl g`

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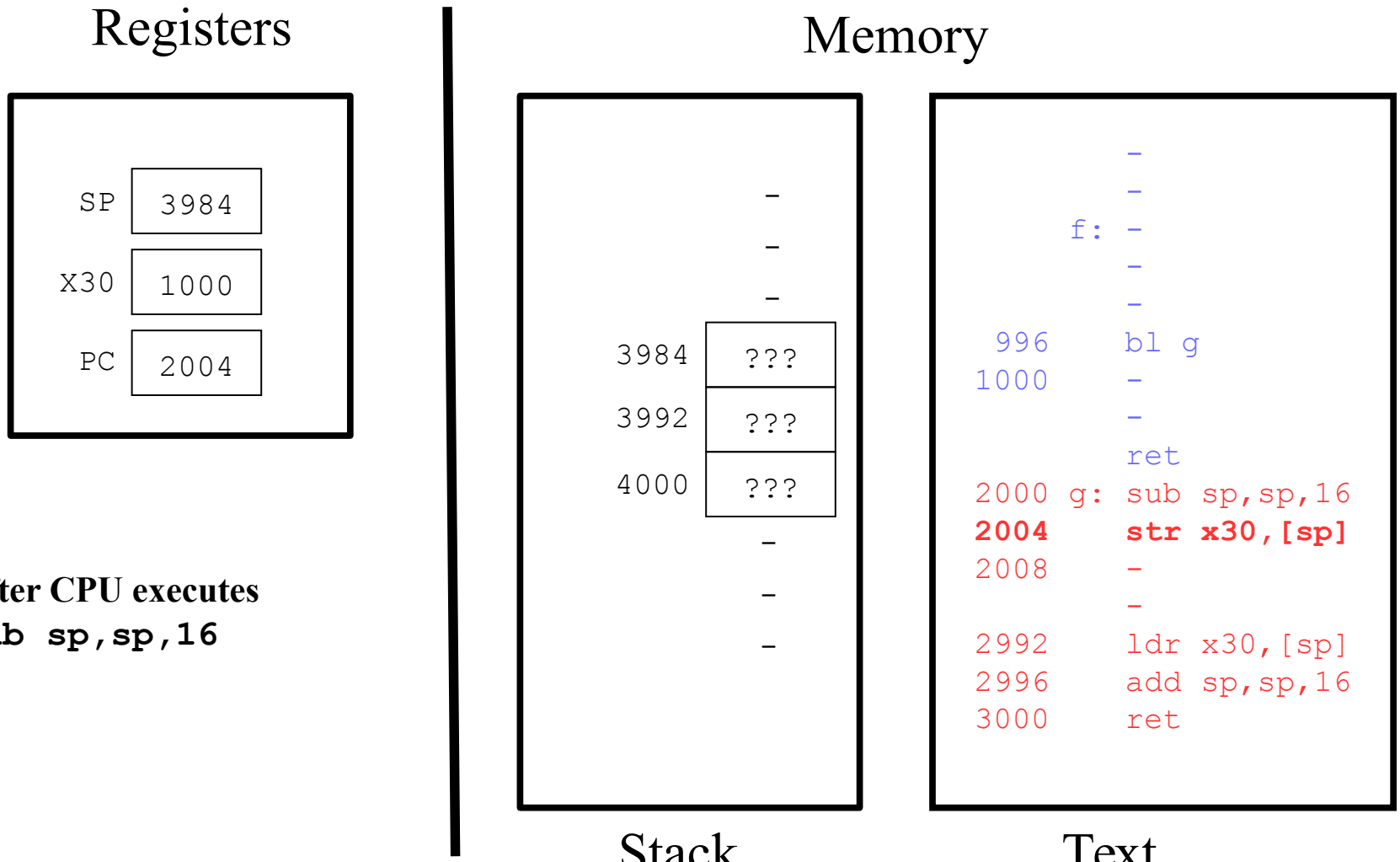

After CPU executes
`sub sp,sp,16`

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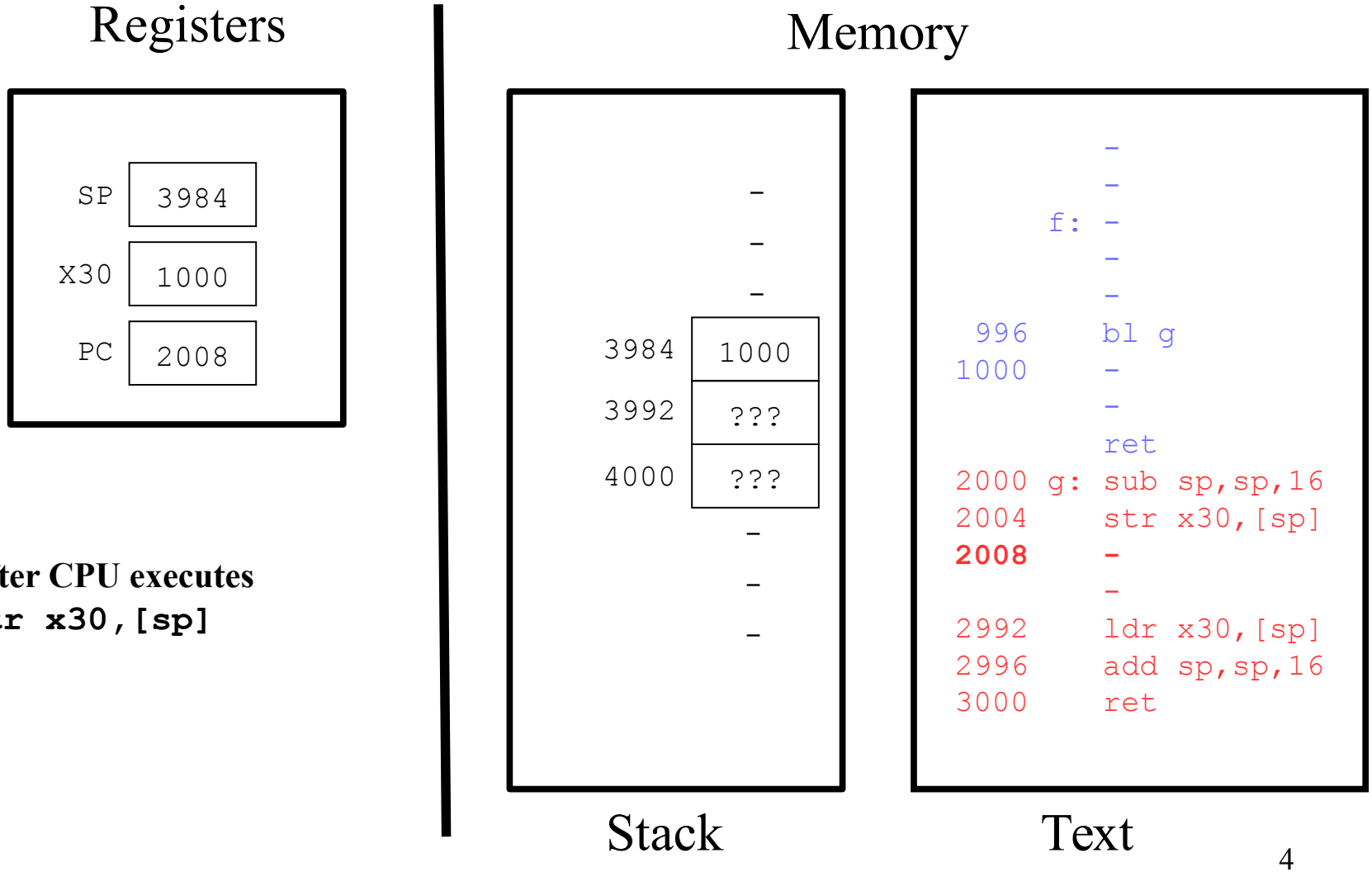

After CPU executes
`str x30,[sp]`

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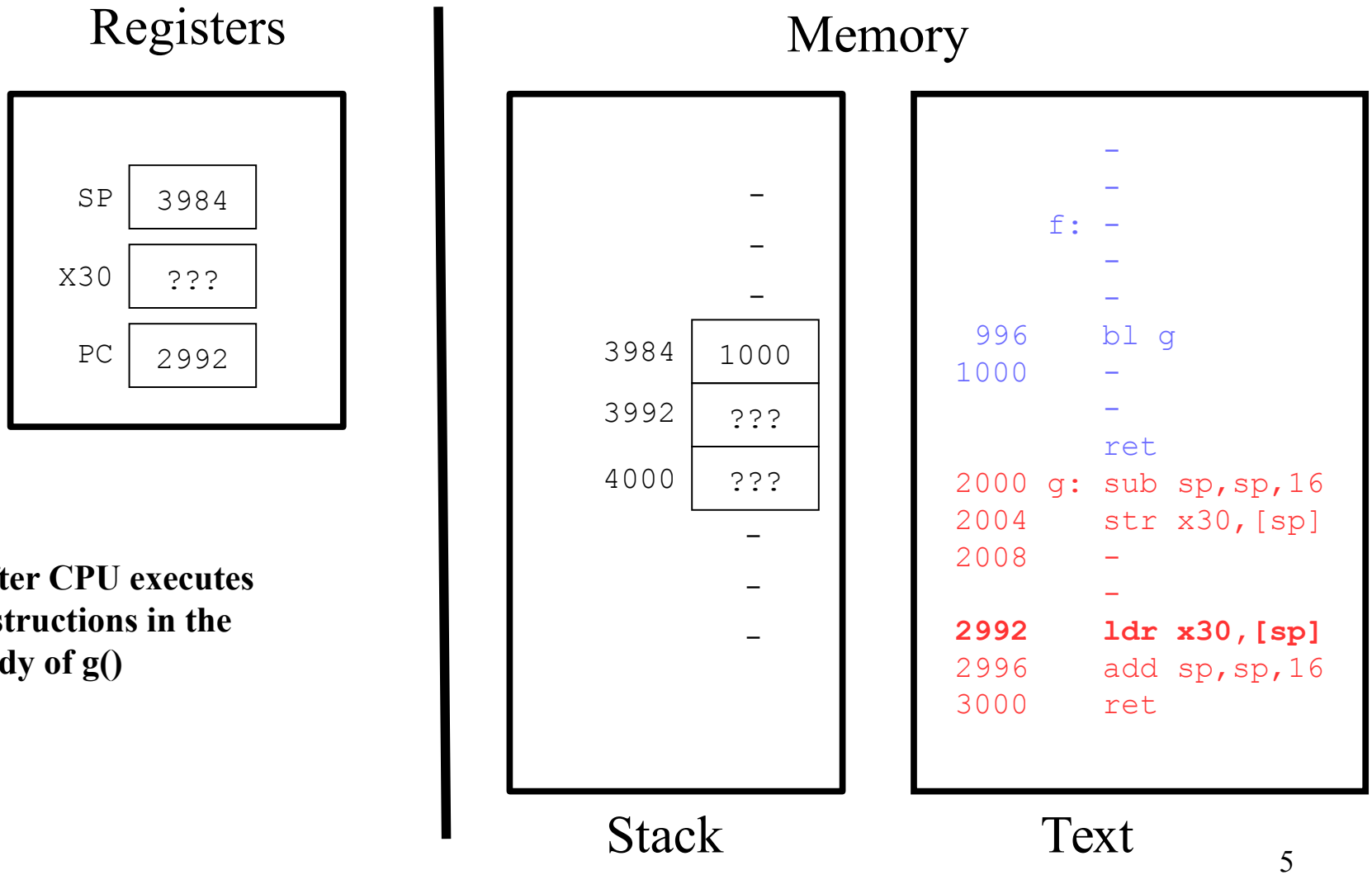

After CPU executes instructions in the body of g()

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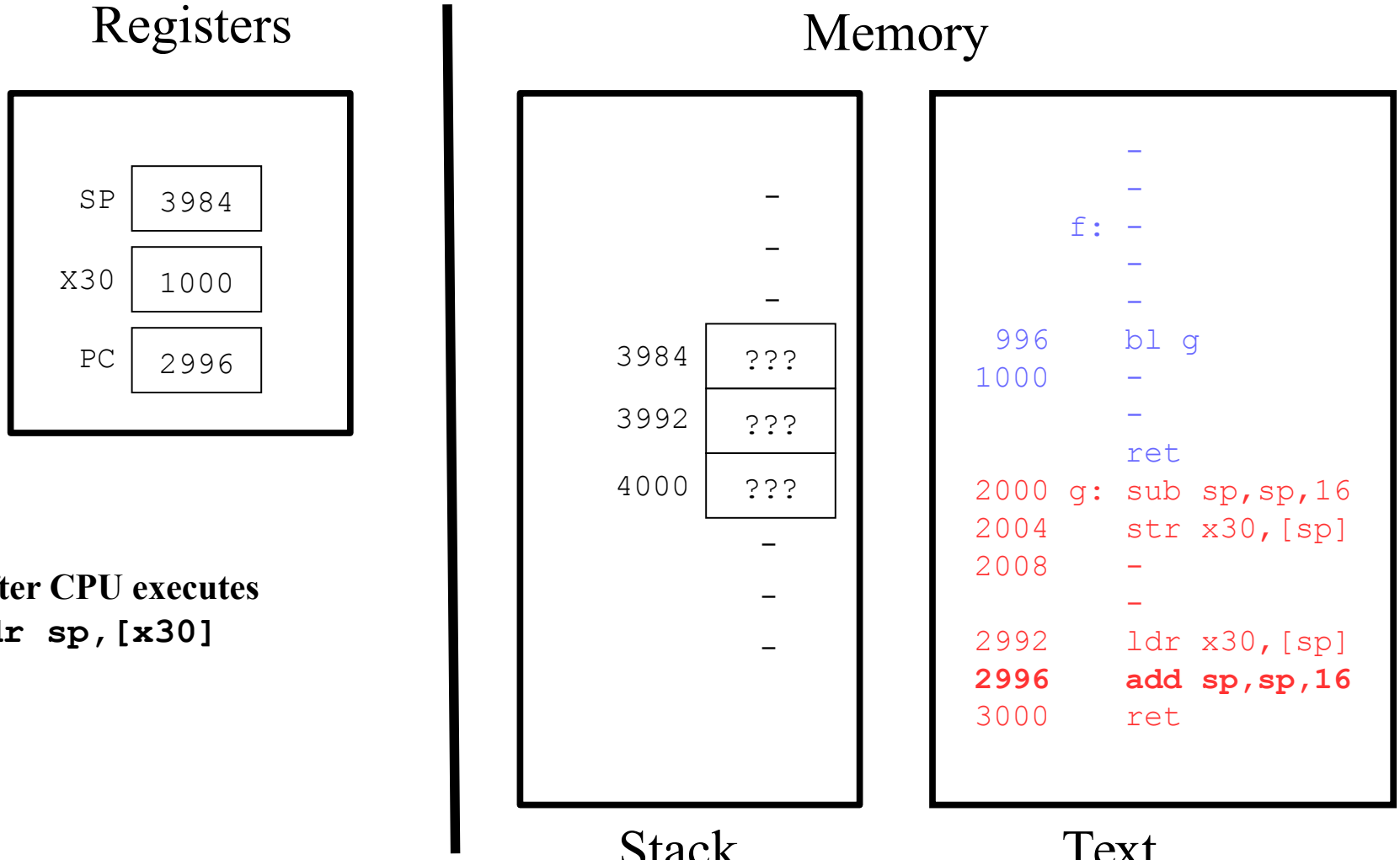

After CPU executes
`ldr sp,[x30]`

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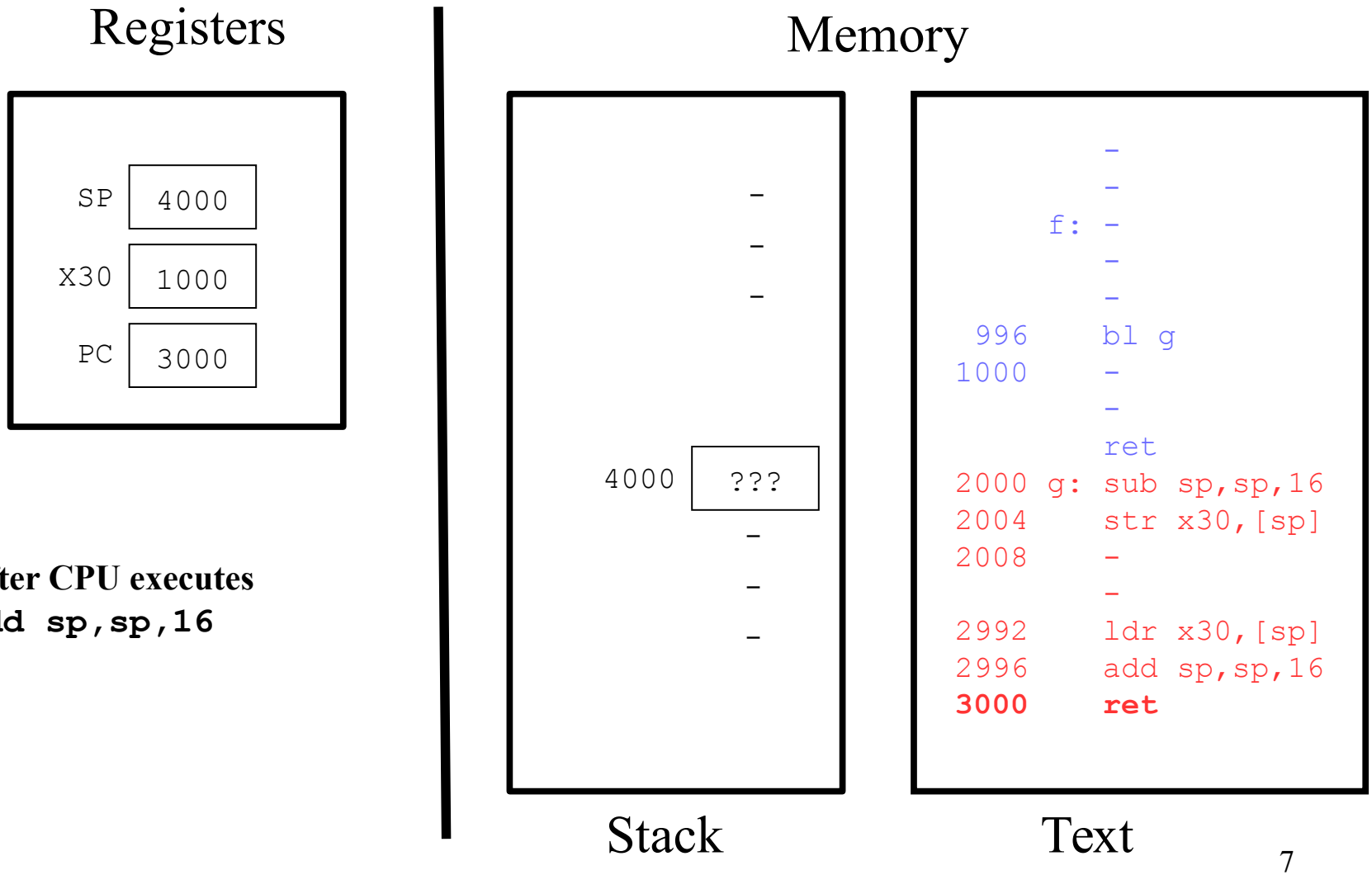

After CPU executes
`add sp,sp,16`

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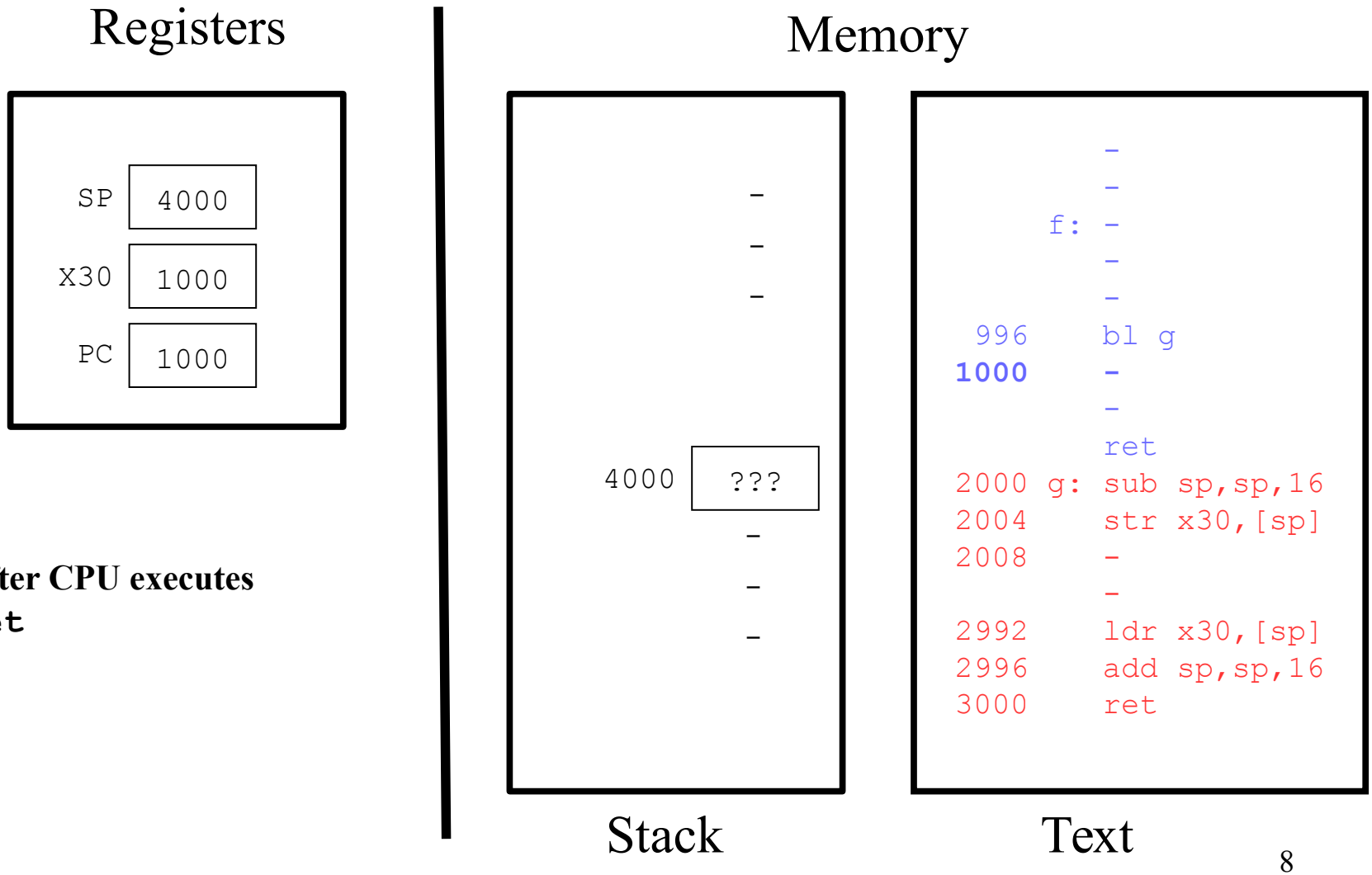

After CPU executes `ret`