

Princeton University

COS 217: Introduction to Programming Systems

Assembler Assignment: 10p2control.s Text Section

Offset	Contents Hex	Contents Binary	Generated by	Explanation
-----	-----	-----	-----	-----
[0]	87	10000111	jmp1 %g1+%g2,%g3	10 00011 111000 00001 0 00000000 00010
[1]	c0	11000000		op rd op3 rs1 i unused rs2
[2]	40	01000000		
[3]	02	00000010		
[4]	01	00000001	nop	00 00000 100 000000000000000000000000
[5]	00	00000000		op rd op2 imm22
[6]	00	00000000		
[7]	00	00000000		
[8]	87	10000111	jmp1 %g1+2,%g3	10 00011 111000 00001 1 00000000000010
[9]	c0	11000000		op rd op2 rs1 i simm13
[10]	60	01100000		
[11]	02	00000010		
[12]	01	00000001	nop	00 00000 100 000000000000000000000000
[13]	00	00000000		op rd op2 imm22
[14]	00	00000000		
[15]	00	00000000		
[16]	87	10000111	jmp1 %g1-4,%g3	10 00011 111000 00001 1 11111111111100
[17]	c0	11000000		op rd op2 rs1 i simm13
[18]	7f	01111111		
[19]	fc	11111100		
[20]	01	00000001	nop	00 00000 100 000000000000000000000000
[21]	00	00000000		op rd op2 imm22
[22]	00	00000000		
[23]	00	00000000		
[24]	87	10000111	jmp1 2+g2,%g3	10 00011 111000 00010 1 00000000000010
[25]	c0	11000000		op rd op2 rs1 i simm13
[26]	a0	10100000		
[27]	02	00000010		
[28]	01	00000001	nop	00 00000 100 000000000000000000000000
[29]	00	00000000		op rd op2 imm22
[30]	00	00000000		
[31]	00	00000000		
[32]	87	10000111	jmp1 %g1,%g3	10 00011 111000 00001 0 00000000 00000
[33]	c0	11000000		op rd op3 rs1 i unused rs2
[34]	40	01000000		
[35]	00	00000000		
[36]	01	00000001	nop	00 00000 100 000000000000000000000000
[37]	00	00000000		op rd op2 imm22
[38]	00	00000000		
[39]	00	00000000		
[40]	87	10000111	jmp1 2,%g3	10 00011 111000 00000 1 00000000000010
[41]	c0	11000000		op rd op2 rs1 i simm13
[42]	20	00100000		
[43]	02	00000010		
[44]	01	00000001	nop	00 00000 100 000000000000000000000000
[45]	00	00000000		op rd op2 imm22
[46]	00	00000000		
[47]	00	00000000		
[48]	07	00000111	sethi 2,%g3	00 00011 100 000000000000000000000010
[49]	00	00000000		op rd op2 imm22
[50]	00	00000000		
[51]	02	00000010		
[52]	07	00000111	sethi -4,%g3	00 00011 100 111111111111111111111100
[53]	3f	00111111		op rd op2 imm22
[54]	ff	11111111		
[55]	fc	11111100		

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[56] 87      10000111 save %g1,%g2,%g3  10 00011 111100 00001 0 00000000 00010
[57] e0      11100000                op rd   op3   rs1   i unused  rs2
[58] 40      01000000
[59] 02      00000010
[60] 87      10000111 save %g1,2,%g3  10 00011 111100 00001 1 00000000000010
[61] e0      11100000                op rd   op3   rs1   i simml3
[62] 60      01100000
[63] 02      00000010
[64] 87      10000111 restore %g1,%g2,%g3 10 00011 111101 00001 0 00000000 00010
[65] e8      11101000                op rd   op3   rs1   i unused  rs2
[66] 40      01000000
[67] 02      00000010
[68] 87      10000111 restore %g1,2,%g3 10 00011 111101 00001 1 00000000000010
[69] e8      11101000                op rd   op3   rs1   i simml3
[70] 60      01100000
[71] 02      00000010

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