

Princeton University

COS 217: Introduction to Programming Systems

Assembler Assignment: 04opcode.s Text Section

Loc	Cont	Contents	Generated by	Explanation
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[0]	86	10000110	add %r1, %r2, %r3	10 00011 000000 00001 0 00000000 00010
[1]	00	00000000		op rd op3 rs1 i unused rs2
[2]	40	01000000		
[3]	02	00000010		
[4]	86	10000110	addcc %r1, %r2, %r3	10 00011 010000 00001 0 00000000 00010
[5]	80	10000000		op rd op3 rs1 i unused rs2
[6]	40	01000000		
[7]	02	00000010		
...				
[36]	86	10000110	and %r1, %r2, %r3	10 00011 000001 00001 0 00000000 00010
[37]	08	00001000		op rd op3 rs1 i unused rs2
[38]	40	01000000		
[39]	02	00000010		
...				
[84]	87	10000111	sll %r1, %r2, %r3	10 00011 100101 00001 0 00000000 00010
[85]	28	00101000		op rd op3 rs1 i unused rs2
[86]	40	01000000		
[87]	02	00000010		
[96]	c6	11000110	ldub [%r1+%r2], %r3	11 00011 000001 00001 0 00000000 00010
[97]	08	00001000		op rd op3 rs1 i asi rs2
[98]	40	01000000		
[99]	02	00000010		
...				
[124]	c6	11000110	stb %r3, [%r1+%r2]	11 00011 000101 00001 0 00000000 00010
[125]	28	00101000		op rd op3 rs1 i asi rs2
[126]	40	01000000		
[127]	02	00000010		
...				
[140]	10	00010000	ba label	00 0 1000 010 1111111111111111011101
[141]	bf	10111111		op a cond disp22 (-35)
[142]	ff	11111111		
[143]	dd	11011101		
[144]	01	00000001	nop	00 00000 100 0000000000000000000000
[145]	00	00000000		op rd imm22
[146]	00	00000000		
[147]	00	00000000		
...				
[284]	91	10010001	ta %r1 + %r2	10 0 1000 111010 00001 0 00000000 00010
[285]	d0	11010000		op cond rs1 i reserved rs2
[286]	40	01000000		
[287]	02	00000010		
[288]	7f	01111111	call label	01 1111111111111111111111110111000
[289]	ff	11111111		op disp30 (-72)
[290]	ff	11111111		
[291]	b8	10111000		
...				
[304]	07	00000111	sethi 2, %r3	00 00011 100 00000000000000000000010
[305]	00	00000000		op rd imm22
[306]	00	00000000		
[307]	02	00000010		
...				