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$ cat 08testmovb.s
## =====
## 08testmovb.s
## =====

.section ".text"

movb    $0, %al
movb    $0, %ah
movb    $0, %bl
movb    $0, %bh
movb    $0, %cl
movb    $0, %ch
movb    $0, %dl
movb    $0, %dh

movb    $1, %al
movb    $1, %ah
movb    $1, %bl
movb    $1, %bh
movb    $1, %cl
movb    $1, %ch
movb    $1, %dl
movb    $1, %dh

movb    $2, %al
movb    $2, %ah
movb    $2, %bl
movb    $2, %bh
movb    $2, %cl
movb    $2, %ch
movb    $2, %dl
movb    $2, %dh

movb    $-2, %al
movb    $-2, %ah
movb    $-2, %bl
movb    $-2, %bh
movb    $-2, %cl
movb    $-2, %ch
movb    $-2, %dl
movb    $-2, %dh

movb    %al, %al
movb    %al, %ah
movb    %al, %bl
movb    %al, %bh
movb    %al, %cl
movb    %al, %ch
movb    %al, %dl
movb    %al, %dh

movb    %al, %al
movb    %ah, %al
movb    %bl, %al
movb    %bh, %al
movb    %cl, %al
movb    %ch, %al
movb    %dl, %al
movb    %dh, %al

movb    %al, 0(%eax)
movb    %al, 0(%ebx)
movb    %al, 0(%ecx)
movb    %al, 0(%edx)
movb    %al, 0(%esi)
movb    %al, 0(%edi)
movb    %al, 0(%ebp)
# movb %al, 0(%esp) # Intel handles as a special case.
# Your assembler need not handle it.

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movb    %al, 1(%eax)
movb    %al, 1(%ebx)
movb    %al, 1(%ecx)
movb    %al, 1(%edx)
movb    %al, 1(%esi)
movb    %al, 1(%edi)
movb    %al, 1(%ebp)
# movb %al, 1(%esp) # Intel handles as a special case.
# Your assembler need not handle it.

movb    %al, 2(%eax)
movb    %al, 2(%ebx)
movb    %al, 2(%ecx)
movb    %al, 2(%edx)
movb    %al, 2(%esi)
movb    %al, 2(%edi)
movb    %al, 2(%ebp)
# movb %al, 2(%esp) # Intel handles as a special case.
# Your assembler need not handle it.

movb    %al, -2(%eax)
movb    %al, -2(%ebx)
movb    %al, -2(%ecx)
movb    %al, -2(%edx)
movb    %al, -2(%esi)
movb    %al, -2(%edi)
movb    %al, -2(%ebp)
# movb %al, -2(%esp) # Intel handles as a special case.
# Your assembler need not handle it.

movb    %al, 321(%eax)
movb    %al, 321(%ebx)
movb    %al, 321(%ecx)
movb    %al, 321(%edx)
movb    %al, 321(%esi)
movb    %al, 321(%edi)
movb    %al, 321(%ebp)
# movb %al, 321(%esp) # Intel handles as a special case.
# Your assembler need not handle it.

movb    %al, 321(%eax)
movb    %ah, 321(%eax)
movb    %bl, 321(%eax)
movb    %bh, 321(%eax)
movb    %cl, 321(%eax)
movb    %ch, 321(%eax)
movb    %dl, 321(%eax)
movb    %dh, 321(%eax)

movb    0(%eax), %al
movb    0(%eax), %ah
movb    0(%eax), %bl
movb    0(%eax), %bh
movb    0(%eax), %cl
movb    0(%eax), %ch
movb    0(%eax), %dl
movb    0(%eax), %dh

movb    1(%eax), %al
movb    1(%ebx), %al
movb    1(%ecx), %al
movb    1(%edx), %al
movb    1(%esi), %al
movb    1(%edi), %al
movb    1(%ebp), %al
# movb 1(%esp), %al # Intel handles as a special case.
# Your assembler need not handle it.

movb    2(%eax), %al
movb    2(%ebx), %al
movb    2(%ecx), %al

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movb    2(%edx), %al
movb    2(%esi), %al
movb    2(%edi), %al
movb    2(%ebp), %al
# movb 2(%esp), %al # Intel handles as a special case.
# Your assembler need not handle it.

movb    -2(%eax), %al
movb    -2(%ebx), %al
movb    -2(%ecx), %al
movb    -2(%edx), %al
movb    -2(%esi), %al
movb    -2(%edi), %al
movb    -2(%ebp), %al
# movb -2(%esp), %al # Intel handles as a special case.
# Your assembler need not handle it.

movb    321(%eax), %al
movb    321(%eax), %ah
movb    321(%eax), %bl
movb    321(%eax), %bh
movb    321(%eax), %cl
movb    321(%eax), %ch
movb    321(%eax), %dl
movb    321(%eax), %dh

movb    321(%eax), %al
movb    321(%ebx), %al
movb    321(%ecx), %al
movb    321(%edx), %al
movb    321(%esi), %al
movb    321(%edi), %al
movb    321(%ebp), %al
# movb 321(%esp), %al # Intel handles as a special case.
# Your assembler need not handle it.

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$ assembler -o 08testmovb.o 08testmovb.s
$ objdump -d 08testmovb.o

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08testmovb.o:      file format elf32-i386
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Disassembly of section .text:
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00000000 <.text>:
 0: b0 00          mov    $0x0,%al
 2: b4 00          mov    $0x0,%ah
 4: b3 00          mov    $0x0,%bl
 6: b7 00          mov    $0x0,%bh
 8: b1 00          mov    $0x0,%cl
 a: b5 00          mov    $0x0,%ch
 c: b2 00          mov    $0x0,%dl
 e: b6 00          mov    $0x0,%dh
10: b0 01          mov    $0x1,%al
12: b4 01          mov    $0x1,%ah
14: b3 01          mov    $0x1,%bl
16: b7 01          mov    $0x1,%bh
18: b1 01          mov    $0x1,%cl
1a: b5 01          mov    $0x1,%ch
1c: b2 01          mov    $0x1,%dl
1e: b6 01          mov    $0x1,%dh
20: b0 02          mov    $0x2,%al
22: b4 02          mov    $0x2,%ah
24: b3 02          mov    $0x2,%bl
26: b7 02          mov    $0x2,%bh
28: b1 02          mov    $0x2,%cl
2a: b5 02          mov    $0x2,%ch
2c: b2 02          mov    $0x2,%dl
2e: b6 02          mov    $0x2,%dh
30: b0 fe          mov    $0xfe,%al
32: b4 fe          mov    $0xfe,%ah
34: b3 fe          mov    $0xfe,%bl

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36:	b7 fe	mov \$0xfe,%bh
38:	b1 fe	mov \$0xfe,%cl
3a:	b5 fe	mov \$0xfe,%ch
3c:	b2 fe	mov \$0xfe,%dl
3e:	b6 fe	mov \$0xfe,%dh
40:	88 c0	mov %al,%al
42:	88 c4	mov %al,%ah
44:	88 c3	mov %al,%bl
46:	88 c7	mov %al,%bh
48:	88 c1	mov %al,%cl
4a:	88 c5	mov %al,%ch
4c:	88 c2	mov %al,%dl
4e:	88 c6	mov %al,%dh
50:	88 c0	mov %al,%al
52:	88 e0	mov %ah,%al
54:	88 d8	mov %bl,%al
56:	88 f8	mov %bh,%al
58:	88 c8	mov %cl,%al
5a:	88 e8	mov %ch,%al
5c:	88 d0	mov %dl,%al
5e:	88 f0	mov %dh,%al
60:	88 80 00 00 00 00	mov %al,0x0(%eax)
66:	88 83 00 00 00 00	mov %al,0x0(%ebx)
6c:	88 81 00 00 00 00	mov %al,0x0(%ecx)
72:	88 82 00 00 00 00	mov %al,0x0(%edx)
78:	88 86 00 00 00 00	mov %al,0x0(%esi)
7e:	88 87 00 00 00 00	mov %al,0x0(%edi)
84:	88 85 00 00 00 00	mov %al,0x0(%ebp)
8a:	88 80 01 00 00 00	mov %al,0x1(%eax)
90:	88 83 01 00 00 00	mov %al,0x1(%ebx)
96:	88 81 01 00 00 00	mov %al,0x1(%ecx)
9c:	88 82 01 00 00 00	mov %al,0x1(%edx)
a2:	88 86 01 00 00 00	mov %al,0x1(%esi)
a8:	88 87 01 00 00 00	mov %al,0x1(%edi)
ae:	88 85 01 00 00 00	mov %al,0x1(%ebp)
b4:	88 80 02 00 00 00	mov %al,0x2(%eax)
ba:	88 83 02 00 00 00	mov %al,0x2(%ebx)
c0:	88 81 02 00 00 00	mov %al,0x2(%ecx)
c6:	88 82 02 00 00 00	mov %al,0x2(%edx)
cc:	88 86 02 00 00 00	mov %al,0x2(%esi)
d2:	88 87 02 00 00 00	mov %al,0x2(%edi)
d8:	88 85 02 00 00 00	mov %al,0x2(%ebp)
de:	88 80 fe ff ff ff	mov %al,0xffffffff(%eax)
e4:	88 83 fe ff ff ff	mov %al,0xffffffff(%ebx)
ea:	88 81 fe ff ff ff	mov %al,0xffffffff(%ecx)
f0:	88 82 fe ff ff ff	mov %al,0xffffffff(%edx)
f6:	88 86 fe ff ff ff	mov %al,0xffffffff(%esi)
fc:	88 87 fe ff ff ff	mov %al,0xffffffff(%edi)
102:	88 85 fe ff ff ff	mov %al,0xffffffff(%ebp)
108:	88 80 41 01 00 00	mov %al,0x141(%eax)
10e:	88 83 41 01 00 00	mov %al,0x141(%ebx)
114:	88 81 41 01 00 00	mov %al,0x141(%ecx)
11a:	88 82 41 01 00 00	mov %al,0x141(%edx)
120:	88 86 41 01 00 00	mov %al,0x141(%esi)
126:	88 87 41 01 00 00	mov %al,0x141(%edi)
12c:	88 85 41 01 00 00	mov %al,0x141(%ebp)
132:	88 80 41 01 00 00	mov %al,0x141(%eax)
138:	88 a0 41 01 00 00	mov %ah,0x141(%eax)
13e:	88 98 41 01 00 00	mov %bl,0x141(%eax)
144:	88 b8 41 01 00 00	mov %bh,0x141(%eax)
14a:	88 88 41 01 00 00	mov %cl,0x141(%eax)
150:	88 a8 41 01 00 00	mov %ch,0x141(%eax)
156:	88 90 41 01 00 00	mov %dl,0x141(%eax)
15c:	88 b0 41 01 00 00	mov %dh,0x141(%eax)
162:	8a 80 00 00 00 00	mov 0x0(%eax),%al
168:	8a a0 00 00 00 00	mov 0x0(%eax),%ah
16e:	8a 98 00 00 00 00	mov 0x0(%eax),%bl
174:	8a b8 00 00 00 00	mov 0x0(%eax),%bh
17a:	8a 88 00 00 00 00	mov 0x0(%eax),%cl
180:	8a a8 00 00 00 00	mov 0x0(%eax),%ch
186:	8a 90 00 00 00 00	mov 0x0(%eax),%dl

18c:	8a b0 00 00 00 00	mov 0x0(%eax),%dh
192:	8a 80 01 00 00 00	mov 0x1(%eax),%al
198:	8a 83 01 00 00 00	mov 0x1(%ebx),%al
19e:	8a 81 01 00 00 00	mov 0x1(%ecx),%al
1a4:	8a 82 01 00 00 00	mov 0x1(%edx),%al
1aa:	8a 86 01 00 00 00	mov 0x1(%esi),%al
1b0:	8a 87 01 00 00 00	mov 0x1(%edi),%al
1b6:	8a 85 01 00 00 00	mov 0x1(%ebp),%al
1bc:	8a 80 02 00 00 00	mov 0x2(%eax),%al
1c2:	8a 83 02 00 00 00	mov 0x2(%ebx),%al
1c8:	8a 81 02 00 00 00	mov 0x2(%ecx),%al
1ce:	8a 82 02 00 00 00	mov 0x2(%edx),%al
1d4:	8a 86 02 00 00 00	mov 0x2(%esi),%al
1da:	8a 87 02 00 00 00	mov 0x2(%edi),%al
1e0:	8a 85 02 00 00 00	mov 0x2(%ebp),%al
1e6:	8a 80 fe ff ff ff	mov 0xfffffff(%eax),%al
1ec:	8a 83 fe ff ff ff	mov 0xfffffff(%ebx),%al
1f2:	8a 81 fe ff ff ff	mov 0xfffffff(%ecx),%al
1f8:	8a 82 fe ff ff ff	mov 0xfffffff(%edx),%al
1fe:	8a 86 fe ff ff ff	mov 0xfffffff(%esi),%al
204:	8a 87 fe ff ff ff	mov 0xfffffff(%edi),%al
20a:	8a 85 fe ff ff ff	mov 0xfffffff(%ebp),%al
210:	8a 80 41 01 00 00	mov 0x141(%eax),%al
216:	8a a0 41 01 00 00	mov 0x141(%eax),%ah
21c:	8a 98 41 01 00 00	mov 0x141(%eax),%bl
222:	8a b8 41 01 00 00	mov 0x141(%eax),%bh
228:	8a 88 41 01 00 00	mov 0x141(%eax),%cl
22e:	8a a8 41 01 00 00	mov 0x141(%eax),%ch
234:	8a 90 41 01 00 00	mov 0x141(%eax),%dl
23a:	8a b0 41 01 00 00	mov 0x141(%eax),%dh
240:	8a 80 41 01 00 00	mov 0x141(%eax),%al
246:	8a 83 41 01 00 00	mov 0x141(%ebx),%al
24c:	8a 81 41 01 00 00	mov 0x141(%ecx),%al
252:	8a 82 41 01 00 00	mov 0x141(%edx),%al
258:	8a 86 41 01 00 00	mov 0x141(%esi),%al
25e:	8a 87 41 01 00 00	mov 0x141(%edi),%al
264:	8a 85 41 01 00 00	mov 0x141(%ebp),%al