

\$ cat 04testaddsub.s

```
## =====  
## 04testaddsub.s  
## =====
```

.section ".text"

```
addl    %eax, %eax  
addl    %eax, %ebx  
addl    %eax, %ecx  
addl    %eax, %edx  
addl    %eax, %esi  
addl    %eax, %edi  
addl    %eax, %ebp  
addl    %eax, %esp
```

```
addl    %eax, %eax  
addl    %ebx, %eax  
addl    %ecx, %eax  
addl    %edx, %eax  
addl    %esi, %eax  
addl    %edi, %eax  
addl    %ebp, %eax  
addl    %esp, %eax
```

```
subl    %eax, %eax  
subl    %eax, %ebx  
subl    %eax, %ecx  
subl    %eax, %edx  
subl    %eax, %esi  
subl    %eax, %edi  
subl    %eax, %ebp  
subl    %eax, %esp
```

```
subl    %eax, %eax  
subl    %ebx, %eax  
subl    %ecx, %eax  
subl    %edx, %eax  
subl    %esi, %eax  
subl    %edi, %eax  
subl    %ebp, %eax  
subl    %esp, %eax
```

\$ assembler -o 04testaddsub.o 04testaddsub.s

\$ objdump -d 04testaddsub.o

04testaddsub.o: file format elf32-i386

Disassembly of section .text:

00000000 <.text>:

```
0: 01 c0          add    %eax,%eax  
2: 01 c3          add    %eax,%ebx  
4: 01 c1          add    %eax,%ecx  
6: 01 c2          add    %eax,%edx  
8: 01 c6          add    %eax,%esi  
a: 01 c7          add    %eax,%edi  
c: 01 c5          add    %eax,%ebp  
e: 01 c4          add    %eax,%esp  
10: 01 c0          add    %eax,%eax  
12: 01 d8          add    %ebx,%eax  
14: 01 c8          add    %ecx,%eax  
16: 01 d0          add    %edx,%eax  
18: 01 f0          add    %esi,%eax  
1a: 01 f8          add    %edi,%eax  
1c: 01 e8          add    %ebp,%eax  
1e: 01 e0          add    %esp,%eax  
20: 29 c0          sub    %eax,%eax  
22: 29 c3          sub    %eax,%ebx  
24: 29 c1          sub    %eax,%ecx  
26: 29 c2          sub    %eax,%edx
```

28:	29	c6	sub	%eax,%esi
2a:	29	c7	sub	%eax,%edi
2c:	29	c5	sub	%eax,%ebp
2e:	29	c4	sub	%eax,%esp
30:	29	c0	sub	%eax,%eax
32:	29	d8	sub	%ebx,%eax
34:	29	c8	sub	%ecx,%eax
36:	29	d0	sub	%edx,%eax
38:	29	f0	sub	%esi,%eax
3a:	29	f8	sub	%edi,%eax
3c:	29	e8	sub	%ebp,%eax
3e:	29	e0	sub	%esp,%eax